**FINAL PROGRAM**

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### MORNING SESSION

**9:00**

**Welcome**

**9:10 – 10:30**

**Session 1: Architecture-based Analysis and Testing**

**“Analyzing Architectural Styles with Alloy”**  
Jung Soo Kim (Carnegie Mellon University)  
David Garlan (Carnegie Mellon University)

**“Towards an architectural approach for the dynamic and automatic composition of software components”**  
Antonio Bucchiarone (IMT Graduate School and Istituto "Alessandro Faedo", CNR)  
Andrea Polini (Istituto di Scienza e Tecnologie della Informazione "Alessandro Faedo", CNR)  
Patrizio Pelliccione (Dipartimento di Informatica, Università dell'Aquila)  
Massimo Tivoli (Dipartimento di Informatica, Università dell'Aquila)

**“Architecture-based Testing Using Goals and Plans”**  
Kristina Winbladh (Dept. of Informatics, Donald Bren School of Information and Comp. Sciences)  
Hadar Ziv (Dept. of Informatics, Donald Bren School of Information and Computer Sciences)  
Debra Richardson (Dept. of Informatics, Donald Bren School of Information and Comp. Sciences)

**10:30 – 11:00**

**Coffee break & Poster Session (**)**

**11:00 – 11:40**

**Session 2: Non functional Modeling and Analysis**

**“Towards Supporting the Architecture Design Process Through Evaluation of Design Alternatives”**  
Lihua Xu (Institute for Software Research, University of California, Irvine)  
Scott A. Hendrickson (Institute for Software Research, University of California, Irvine)  
Eric Hettwer (Institute for Software Research, University of California, Irvine)  
Hadar Ziv (Institute for Software Research, University of California, Irvine)  
André van der Hoek (Institute for Software Research, University of California, Irvine)  
Debra J. Richardson (Institute for Software Research, University of California, Irvine)
"Performance Implications of Design Pattern Usage in Distributed Applications - Case Studies in J2EE and .NET"
Jakub Rudzki (Solita Oy, Satakunnankatu 18 A, Finland)
Tarja Systa (Tampere University of Technology)

11:40 – 12:30
Session 3: Round Table Discussion

AFTERNOON SESSION

1:45
Re-opening

2:00 – 3:00
Session 4: Software Product Line Modeling and Testing

"A Foundation for Software Behavioural Conformance in Software Product Line Architectures"
Dario Fischbein (Department of Computing, Imperial College)
Sebastian Uchitel (Department of Computing, Imperial College)
Victor Braberman (Department of Computing, Universidad de Buenos Aires)

"Coverage and Adequacy in Software Product Line Testing"
Myra B. Cohen (Department of Computer Science and Engineering, University of Nebraska)
Matthew B. Dwyer (Department of Computer Science and Engineering, University of Nebraska)
Jiangfan Shi (Department of Computer Science and Engineering, University of Nebraska)

"Making Testing Product Lines More Efficient by Improving the Testability of Product Line Architectures"
Ronny Kolb (Fraunhofer Institute for Experimental, Software Engineering (IESE))
Dirk Muthig  (Fraunhofer Institute for Experimental, Software Engineering (IESE))

3:00 – 3:30
Session 5: Tool Demo

"Incorporating Behavior Analysis into EASEL"
Presenter: Scott A. Hendrickson (Institute for Software Research, University of California, Irvine)

"Analyzing Architectural Styles with Alloy"
Presenter: Jung Soo Kim (Carnegie Mellon University)

3:30 – 4:00
Coffee break & Poster Session (*)

4:00 – 5:30
Session 6: Round Table Discussion

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"Verification Support for Plug-and-Play Architectural Design"
Shangzhu Wang (Department of Computer Science, University of Massachusetts Amherst)
George S. Avrunin (Department of Computer Science, University of Massachusetts Amherst)
Lori A. Clarke (Department of Computer Science, University of Massachusetts Amherst)

"Application of Built-In-Testing in Component-Based Embedded Systems"
Irena Pavlova (Faculty of Math. and Informatics, Sofia University)
Mikael Åkerholm (CC-Systems and Mälardalen University)
Johan Fredriksson (Mälardalen University)